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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/751,882	12/29/2000	Gralf Gaedeken	450117-02963	8380

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FROMMER LAWRENCE & HAUG
745 FIFTH AVENUE- 10TH FL.
NEW YORK, NY 10151

EXAMINER	
JONES, PRENELL P	

ART UNIT	PAPER NUMBER
2667	

DATE MAILED: 01/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/751,882	GAEDEKEN ET AL.	
	Examiner	Art Unit	
	Prenell P Jones	2667	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *27 September 2004*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-33 and 37-96* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☒ Claim(s) *48-67 and 86-96* is/are allowed.

6)☒ Claim(s) *1,2,26,31-33,37-41,43-47,68-70,72-79 and 81-85* is/are rejected.

7)☐ Claim(s) *3-25, 27-30, 42, 71 and 80* is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

Response to Arguments

1. Applicant's arguments with respect to claims 1-33 and 37-97 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

3. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

4. Claims 1, 2, 26, 31-33 and 37-41, 43-47, 68-70, 72-79 and 81-85, are rejected under 35 U.S.C. 103(a) as being unpatentable over James et al in view of Vonbank et al.

Regarding claims 1, 2, 26 and 31-33, James discloses (Abstract, col. 2, line 18-65, col. 3, line 41 thru col. 4, line 67) a method and system for providing connections in a communication

system that consist of a plurality of communication devices/nodes using stream identifiers, wherein plurality of data buses are utilized that accommodate asynchronous data streams and isochronous data streams, bus bridge used to connect devices, IEEE 1394 bus, whereby communication exist between talker nodes (receive/transmit, listener/talker node, uplink/downlink), isochronous bus connection includes multiple buses, one or more bus bridges, communication media interconnecting buses maybe RF, utilization of extended unique identifier (EUI), register offset associated with the talker/uplink along with stream identifier aids in the communication between talker and listener, (col. 5, line 15 thru col. 6, line 65) control status register (CSR) defines registry and functionality, collection of multiple buses connected through a bus bridge, primary bus and secondary bus, routing packets as associated with packet channel number, (col. 7, line 14 col. 8, line 67) routing decisions are made by checking destination ID addresses and channel numbers/channel ID as associated with the with requester/uplink. James is silent on link layer devices as associated with a distributed system. In analogous art, Vonbank discloses (Abstract, col. 3, line 23 thru col. 4, line 63) plurality of IEEE 1394 buses, data analyzer which is an integrated distributed environment that supports IEEE 1394 standards, 1394 provides data verification across buses, IEEE 1394 supports asynchronous and isochronous data transfers, plurality of link layer modules, (col. 5, line 5-67) data exchange between multiple devices whereby a sender and a receiver (uplink/downlink) are communicating (col. 8, line 17 thru col. 12, line 40) plurality of PCI link layer devices, module bus interface includes link layer modules used to format data into packets for transmission, plurality of IEEE 1394 link layer devices along with associated plurality of buses, link layer module is preferable integrated onto a PCI expansion card. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to implement communicating packet data in a distributed network which consist of link layer interface devices

as taught by Vonbank with the teachings of James for the purpose of providing translation of data packets in a 1394 computer system architecture.

Claims 37-41, 43-47, 68-70, 72-79, 81-85 are rejected under 35 U.S.C. 103(a) as being unpatentable over James et al in view of Vonbank et al

Regarding claims 37-41, 43-47, 68-70, 72-79, 81-85, James discloses (Abstract, col. 2, line 18-65, col. 3, line 41 thru col. 4, line 67) a method and system for providing connections in a communication system that consist of a plurality of communication devices/nodes using stream identifiers, wherein plurality of data buses are utilized that accommodate asynchronous data streams and isochronous data streams, bus bridge used to connect devices, IEEE 1394 bus, whereby communication exist between talker nodes (receive/transmit, listener/talker node, uplink/downlink), isochronous bus connection includes multiple buses, one or more bus bridges, communication media interconnecting buses maybe RF, utilization of extended unique identifier (EUI), register offset associated with the talker/uplink along with stream identifier aids in the communication between talker and listener, (col. 5, line 15 thru col. 6, line 65) control status register (CSR) defines registry and functionality, collection of multiple buses connected through a bus bridge, primary bus and secondary bus, routing packets as associated with packet channel number, (col. 7, line 14 col. 8, line 67) routing decisions are made by checking destination ID addresses and channel numbers/channel ID as associated with the with requester/uplink. James is silent on link layer devices as associated with a distributed system. In analogous art, Vonbank discloses (Abstract, col. 3, line 23 thru col. 4, line 63) plurality of IEEE 1394 buses, data analyzer which is an integrated distributed environment that supports IEEE 1394 standards, 1394 provides data verification across buses, IEEE 1394 supports

asynchronous and isochronous data transfers, plurality of link layer modules, (col. 5, line 5-67) data exchange between multiple devices whereby a sender and a receiver (uplink/downlink) are communicating (col. 8, line 17 thru col. 12, line 40) plurality of PCI link layer devices, module bus interface includes link layer modules used to format data into packets for transmission, plurality of IEEE 1394 link layer devices along with associated plurality of buses, link layer module is preferable integrated onto a PCI expansion card. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to implement communicating packet data in a distributed network which consist of link layer interface devices as taught by Vonbank with the teachings of James for the purpose of providing translation of data packets in a 1394 computer system architecture.

Allowable Subject Matter

5. Claims 48-67 and 86-96 are allowed over prior art.

6. Claims 3-25, 27-30, 42, 71 and 80 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

7. The following is a statement of reasons for the indication of allowable subject matter: Although the cited art discloses a method and system for providing connections in a communication system that consist of a plurality of communication devices/nodes using stream identifiers, wherein plurality of data buses are utilized that accommodate asynchronous data streams and isochronous data streams, bus bridge used to connect devices, IEEE 1394 bus, whereby communication exist between talker nodes (receive/transmit, listener/talker node, uplink/downlink), isochronous bus connection includes multiple buses, one or more bus bridges, communication media interconnecting buses maybe RF, utilization of extended unique identifier,

register offset associated with the talker/uplink along with stream identifier aids in the communication between talker and listener, control status register defines registry and functionality, collection of multiple buses connected through a bus bridge, primary bus and secondary bus, routing packets as associated with packet channel number, routing decisions are made by checking destination ID addresses and channel numbers/channel ID as associated with the with requester/uplink, plurality of IEEE 1394 buses, data analyzer which is an integrated distributed environment that supports IEEE 1394 standards, 1394 provides data verification across buses, IEEE 1394 supports asynchronous and isochronous data transfers, plurality of link layer modules, data exchange between multiple devices whereby a sender and a receiver (uplink/downlink) are communicating plurality of PCI link layer devices, module bus interface includes link layer modules used to format data into packets for transmission, plurality of IEEE 1394 link layer devices along with associated plurality of buses, link layer module is preferable integrated onto a PCI expansion card they fail to teach or suggest a bus enable register identifying a second data bus serving a predefined destination, uplink means includes a second and fourth register that includes destination identifier of each link layer device, an acknowledge code generator that generates an acknowledgement to be sent to the originator of a packet accepted from a data bus it is connected to and transmitted via a transmission path to a destination different to itself, response packet generator, packetizer, packet separator, layer device translates a destination of a packet into a predetermined other destination which is the only further destination of the data bus connected to other destination, and data interface that are associated with a channel that extends across a data interface that is coupled to a first bus via a portal interface, and first data bus and data interface constitute part of a loop-free network.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prenell P. Jones whose telephone number is 571-272-3180. The examiner can normally be reached on 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chi Pham can be reached on 571-272-3179. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Prenell P. Jones

January 19, 2005